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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Geoff W. Taylor et al.

SERIAL NO.: 10/602,217 GROUP ART UNIT: 2874

FILED: June 24, 2003 EXAMINER: Sung H. Pak

FOR: "Interference Cancellation System Employing Photonic Sigma Delta Modulation and Optical True Time Delay" ATT'Y DOCKET: OPE-024

Commissioner of Patents
and Trademarks
P.O. 1450
Alexandria, VA 22313

I hereby certify that this correspondence is being deposited on this day with the United States Postal Service as first class mail in an envelope addressed to : Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Jay P. Sbrollini 9/21/2004
Jay P. Sbrollini Date
Reg. No. 36,266

Sir:

SUBMITTAL OF SUPPLEMENTAL
DOCUMENTS PURSUANT TO DUTY OF DISCLOSURE

Pursuant to applicant's duty of disclosure under 37 CFR Section 1.56, enclosed is a completed form PTOL-1449 as well as copies of the cited documents which relate to the above-referenced patent application. Since this document submittal is being presented prior to the first examination on the merits, no fee is due herewith.

The following foreign documents and articles are attached:

WO 02/071490; 09/12/02; PCT
WO 2004/038812; 05/06/04; PCT

"A Brief Introduction to Sigma Delta Conversion", by David Jarman; May, 1995; discloses the functionality of these converters.

"High Resolution Signal Conditioning ADCs"; by Walt Kester, James Bryant and Joe Buxton; discloses the description and functionality of these high resolution converters.

"Synthesis and Analysis of Sigma-Delta Modulators Employing Continuous-Time Filters"; Philippe Benabes, Mansour Keramat, and Richard Kielbasa; discloses a methodology for analysis and synthesis of lowpass sigma-delta converters.

"Analog-to-Digital Converter Architecture and Choices for System Design"; by Brian Black; discloses four major circuit architectures used in A/D converter design.

"Polymer-Based Optical Waveguide Circuits for Photonic Phased Array Antennas"; Suning Tang, L.Wu, Z.Fu, D.An, Z.Han, and Ray T. Chen; discloses a novel compact detector-switched polymeric waveguide true-time-delay module.

The listed documents are brought to the Examiner's attention because they are known to the applicant and/or the applicant's attorney and may be considered by the Examiner to be material to his/her examination. This listing should not be construed as representation that a search has been made or that no better art exists. No inference should be made that the documents are in fact material merely because they are referenced herein. Moreover, no representation is made that the brief descriptions of the references herein necessarily describe the most material aspects of the references. Further, by this listing, the applicant is not making any admission regarding the relative dates of the invention and listed disclosures.

Respectfully submitted,

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US PATENT DOCUMENTS

Examiner Initials		Document No.	Date	Name	Class	Sub-class	Filing date if approp.
	A						

FOREIGN PATENT DOCUMENTS

Examiner Initials		Document No.	Date	Country	Class	Sub-class	Translation Yes	Translation No
		WO 02/071490	09/12/02	PCT				
		WO 2004/038812	05/06/04	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
		"A Brief Introduction to Sigma Delta Conversion", by David Jarman; Intersil publication; May, 1995
		"High Resolution Signal Conditioning ADCs" by Walt Kester, James Bryant, and Joe Buxton;
		"Synthesis and Analysis of Sigma-Delta Modulators Employing Continuous-Time Filters", by Philippe Benabes, Mansour Keramat, and Richard Kielbasa; Analog Integrated Circuits and Signal Processing
		"Analog-to-Digital Converter Architectures and Choices for System Design"; by Brian Black; Analog Diaogue 33-8 (1999)
		"Polymer-Based Optical Waveguid Circuits for Photonic Phased Array Antennas"; by Suning Tang, L. Wu, Z. Fu, D. An, Z. Han, and Ray T. Chen; January, 1999; Part of the SPIE Conference on Optoelectronic Interconnects VI

EXAMINER	DATE CONSIDERED